

L Number	Hits	Search Text	DB	Time stamp
-	2	6694488.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:30
-	91001	cat anf cag and (system adj on adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:31
-	0	cat and cag and (system adj on adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:32
-	0	cat and graph and (system adj on adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:32
-	0	tun\$4 and graph and (system adj on adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:33
-	0	(architecture near3 topology) and (system adj on adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:34
-	0	(architecture near3 topology) and (system adj2 on adj2 chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:34
-	18	(system adj2 on adj2 chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:36
-	1005	"system-on-chip"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:36
-	21	"system-on-chip" and graph and tun\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:49
-	1	"system-on-chip" and (graph same analy\$4) and tun\$4 and (architecture same topology)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:52
-	5	"system-on-chip" and tun\$4 and (architecture same topology)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 17:02
-	45	"hw/sw" same design	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 17:18

-	13	"hw/sw" and cat	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 17:20
-	14	"hw/sw" and (cat or (communication adj architecture adj tuner))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 17:27
-	8	"hw/sw" and (communication adj architecture)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/01 17:27